Altera Arria V GX Starter Kit BTS 出現 Could notfind devive 5AGGXFB3 問題解決方

A Board Test System	
Configure Help About	
Rover Monitor	System mio GPIO Flash: SSRAM DDR3 XCVR1 XCVR2 Board information Board Name: Arnia V GX Starter Kit Board Board PAN: 6XX-44099R Serial number: NA Factory test version: NA MAC:::: MAX V ver: MAX V registers PSO SRST Use PSR PSR: Use PSS PSS:0
Messages	Block description Address
Connecting to the target java.lang.Exception: Could not find device 5AGGXFB3 This problem could be caused by different version of Quartus. Please ensure the rig	ext_flash 0x0800.0000 - 0EFF.FFFF max5_inf 0x0060.0000 - 0060.01FF onchip_memory 0x0010.0000 - 0017.FFFF sc_inf 0x0000.4000 - 0000.43FF CPU 0x0000.1000 - 0000.17FF high_res_timer 0x0000.0102 - 0000.011F

解決步驟:

- 1. A5SK 連接電源, PC 連接 USB cable 到 A5SK USB-Blaster II, 開啟 A5SK 電源
- 2. 開啟 Quartus II 12.1 SP1
- 3. 開啟 Quartus II 12.1 SP1 的 Programmer 視窗
- 4. 確認 Hardware Setup 有偵測到 USB-BlasterII



5. 點擊 Auto Detect

點選 5AGXFB3H4, 點擊 OK

Select Device
Encounter devices with shared JTAG ID for device 1. Please select your device.
SAGTFD3H3
SAGTMD3G3
SAGXBB3D4
SAGXBB3D6
SAGXFB3H4
SAGXFB3H4ES
SAGXFB3H6
SAGXFB3H6ES
SAGXMB3G4
SAGXMB3G4ES
SAGXMB3G6
SAGXMB3G6ES
ОК

Programmer - [Ch	nain1.cdf]*										-	x
Eile Edit View Pr	ocessing <u>T</u> ools <u>W</u> indow	Help 🐬							5	earch alte	era.com	•
Ardware Setup USB-BlasterII [USB-1] Mode: JTAG Progress: Enable real-time ISP to allow background programming (for MAX II and MAX V devices) Progress: Image: State St												
Start	File	Device	Checksum	Usercode	Program/ Configure	Verify	Blank- Check	Examine	Security Bit	Erase	ISP CLAMP	
Stop	<none> <none></none></none>	5AGXFB3H4 5M2210Z	00000000	<none> <none></none></none>								
Auto Detect												
Add File												

6. 點選 5AGXFB3H4, 點擊 Change File

🐌 Programmer - [Chain1.cdf]* 📃 🗖 💻 🌄												
<u>Eile E</u> dit <u>V</u> iew Pr	rocessing <u>T</u> ools <u>W</u> indo	w <u>H</u> elp 🛡							5	Search alte	era.com	(
Hardware Setup US8-BlasterII [US8-1] Mode: JTAG									ss:			
Start	File	Device	Checksum	Usercode	Program/ Configure	Verify	Blank- Check	Examine	Security Bit	Erase	ISP CLAMP	
	<none></none>	5AGXFB3H4	00000000	<none></none>								
Auto Detect Delete Cange File Constraints	<none></none>	5M2210Z	0000000	<none></none>								
Add Device												

將檔案路徑指到

..\kits\arriaVGX_5agxfb3hf35_start\examples\board_test_system\sof 內的 Bts_config.sof, 點擊 Open

🛬 Select New Programming File
Look in: C:\altera\12.1sp1\kits\arriaVGX_5rt\examples\board_test_system\sof \cdot \
File name: bts_config.sof Open
Files of type: Programming Files (*.sof *.pof *.jam *.jbc *.ekp *.jic) Cancel

7. 5AGXFB3H4F35 勾選 Program/Configure, 點擊 Start 開始燒錄

🐌 Programmer - [Ch	nain1.cdf]*										- 0	×
<u>File E</u> dit <u>V</u> iew Pr	ocessing <u>T</u> ools <u>W</u> indow	Help 🛡							5	Search alte	era.com	-
🔔 Hardware Setup	USB-BlasterII [USB-1]				Mode: JTAG			Progre	iss:			
Enable real-time ISF	P to allow background program	ming (for MAX II and I	MAX V devices)									
Start	File	Device	Checksum	Usercode	Program/ Configure	Verify	Blank- Check	Examine	Security Bit	Erase	ISP CLAMP	IPS
- Hill Stop	C:/altera/12.1sp1/kits/arr	5AGXFB3H4F35	0837039B	FFFFFFF								
Stop	<none></none>	5M2210Z	00000000	<none></none>								
Auto Detect												
Add File												
쌸 Change File												
	•											4
Save File												<u>^</u>
Add Device												E
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8. 確認燒錄完成

👋 Programmer - [Ch	nain1.cdf]*											x			
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Image: An advance Setup USB-BlasterII [USB-1] Mode: JTAG Progress: 100% (Success) Image: Decision of the setup of the setu									uccessful)						
Start	File	Device	Checksum	Usercode	Program/ Configure	Verify	Blank- Check	Examine	Security Bit	Erase	ISP CLAMP	IPS			
	C:/altera/12.1sp1/kits/arr	5AGXFB3H4F35	0837039B	FFFFFFF	V										
Auto Detect	<none></none>	5M2210Z	0000000	<none></none>											

9. 執行 BoardTestSystem.exe

A Board Test System	
Configure Help About	
ADDERA. Arria 2 V Power Monitor	System info GPIO Flash SSRAM DDR3 XCVR1 XCVR2 Board information Board Name: Arria V GX Starter Kit Board Board P/N: 6XX-44099R-0L Serial number: 5AGXSK00005004 Factory test version: 12.1.1.0 MAC: 00:07:ed:20:13:3c MAX V ver: 2
	MAX V registers PSO Use PSR SRST Use PSS PSS: 0 JTAG chein USE-BlasterII on localhost (USB-1) 1 : 5AGT(FD3H31MD3G3)15AGXBB3D41@1 2 - 5M(1270ZF32412210Z)1EPM2210@2
Messages	Qsys Memory Map
Detected the GPIO, SRAM, Flash Project	Buck description Address ext_flash 0x0800.0000 - 0BFF.FFFF max5_inf 0x0000.0000 - 0000.01FF onchip_memory 0x0010.0000 - 0001.7FFF sc_inf 0x0000.0000 - 0000.43FF CPU 0x0000.1000 - 0000.17FF high_res_timer 0x0000.0100 - 0000.011F

10. 测試其它功能,請依上述步驟,先行燒錄對應的 .sof 檔案到 FPGA,再執行 BoardTestSystem.exe

名稱	修改日期	類型	大小
bts_config.sof	2013/3/26 下午 1	SOF 檔案	16,342 KB
bts_ddr3_hard.sof	2013/2/22 上午 1	SOF 檔案	16,348 KB
bts_ddr3_soft.sof	2013/2/18 下午 0	SOF 檔案	16,346 KB
bts_xcvr1.sof	2013/2/20 上午 1	SOF 檔案	16,329 KB
bts_xcvr2.sof	2013/2/14 下午 0	SOF 檔案	16,329 KB