

# Terasic TREX C1 Development Kit

With built-in USB Blaster  
and C++ API

*SURPASS UNSURPASSABLE*

## OUR FPGA, YOUR ASIC

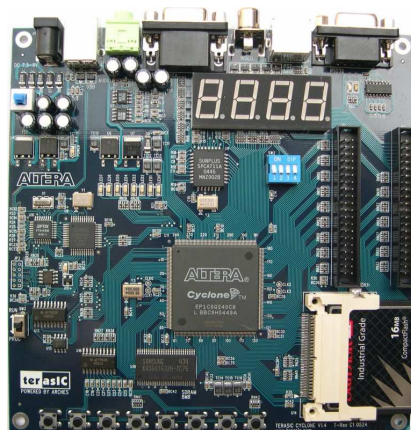
Terasic provides state-of-the-art FPGA prototyping systems using Altera latest 90nm technologies. We provide systems using Altera Stratix II, Cyclone II (2C35), and Cyclone.

TREX C1 board has the highest user recommendation. All our boards are tested with the strictest QC Standard before shipping.

terasic

### Specifications

- Use Altera Cyclone 1C6 FPGA with 6,000 LEs and 240 Pins.
- Built-in USB Blaster Programming/Control Circuits.
- Powered by USB or 9V-DC.
- JTAG/AS Programming Mode.
- Dual Oscillator (50Mhz and 27 Mhz).
- 8Mbyte SDRAM.
- 1Mbyte Flash Memory.
- 4-bit DIP Switch.
- CF Card Socket with IDE Mode.
- VGA DAC for VGA monitor.
- TV Encoder for (NTSC/PAL) Systems.
- Audio DAC for 16-bit CD-Quality Music.
- RS-232 Com Port.
- PS/2 Mouse/Keyboard Connectors.
- 8 User LEDs.
- 4 x 7-SEG Display.
- 8 x Debounced Push Buttons.
- C++ API for Flash. SDRAM. LED 7-SEG. Observe that the VGA Monitor is Showing Color Patterns.



ALTERA

QUARTUS<sup>II</sup>

Nios<sup>II</sup>

USB  
UNIVERSAL SERIAL BUS

Visit [trex.terasic.com](http://trex.terasic.com) for latest update and new lab examples.

### Terasic Lab Contents

Terasic Lab CD-ROM provides you many labs with source code:

- SDRAM Controller
- Flash Controller
- 16-bit CD Music Box
- VGA Graphic Generator
- TV (NTSC/PAL) Controller
- RS-232 Lab and C++ API
- USB-version API
- SDRAM Music Box
- PS-2 Keyboard lab
- CF Card Reader



terasic  
www.terasic.com

Terasic Technologies Inc.  
Website: <http://www.terasic.com/>  
E-Mail: [support@terasic.com](mailto:support@terasic.com)