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tNano

DEVELOPMENT AND EDUCATION KIT

Quick Start Guide ▶▶▶

Altera EPCS4
Configuration Device

2Kb EEPROM

8 Green LEDs

2 Push-buttons

80-pin Edge
Connector

USB-Blaster
Port

ALTERA
Cyclone III
EP3C25F324C6NES
J AACA001813A
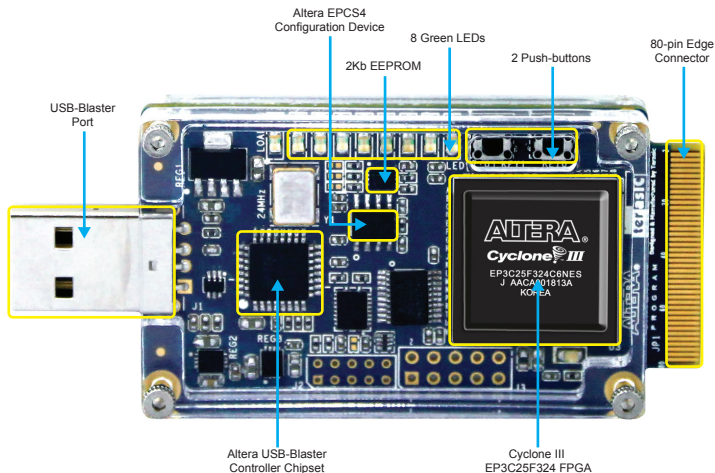KOREA

Altera USB-Blaster
Controller Chipset

Cyclone III
EP3C25F324 FPGA

1 Introduction

Thank you for choosing Terasic tNano kit as your FPGA development platform. This guide offers the quickest way to install the prerequisite software and begin using the tNano board. Many useful resources are available on the website of Terasic.

2 Installing the Altera's software

To begin, install the Quartus II software, which you can download from Altera's website. The tNano board requires Quartus II software version 10.0 or later in order to properly compile and generate FPGA programming files. Additionally, you may need to install the Nios II Embedded Design Suite. Please download the web edition version software of Quartus and Nios II EDS.

Software download address: http://www.altera.com/products/software/sfw-index.jsp.

Perform the following steps to complete the installation:

1. Quartus II FPGA design software
2. Nios II EDS
3. Obtain a license file from Altera if you choose the subscription edition while installing the Quartus II software
4. Install the USB-Blaster driver software as explained in the document install_usb.pdf. which can be found in the user_manual folder in the tNano System CD

3 **P**erform power-on test

1. Connect the USB type-A male connector or a USB extension cable from the tNano board to the PC

2. You should observe the user LEDs flashing

4 **L**aunch Demonstration

If you are familiar with Altera FPGA design flow, you can launch the demonstrations offered in the tNano board CD by performing the following steps:

1. Connect the tNano board to the PC directly or using an extended USB cable

2. Copy all the content of tNano system CD to your host PC; and browse to the directory <install.path>:\demonstrations\<Demo name>\demo batch\

3. Double click the <demo batch>.bat file for downloading configuration data and Nios II software if applicable

5 **L**earn more about the tNano board

If you are a beginner in FPGA design , please refer to the my_first_fpga.pdf and the my_first_niosii.pdf.

The next step is to read the tNano user manual, which contains detailed information about the FPGA device, wiring and functionality information of peripherals, operation instructions and design principles of the offered demonstrations plus how-to basic knowledge of developing user specified applications.

terasic
www.terasic.com

台湾地区	TEL:+886-3-550-8800	大陆地区	TEL:+86-27-87745390
	FAX:+886-3-550-2860		FAX:+86-27-87745392
	sales@terasic.com		sales@terasic.com.cn
	support@terasic.com		support@terasic.com.cn
	http://www.terasic.com.tw		http://www.terasic.com.cn